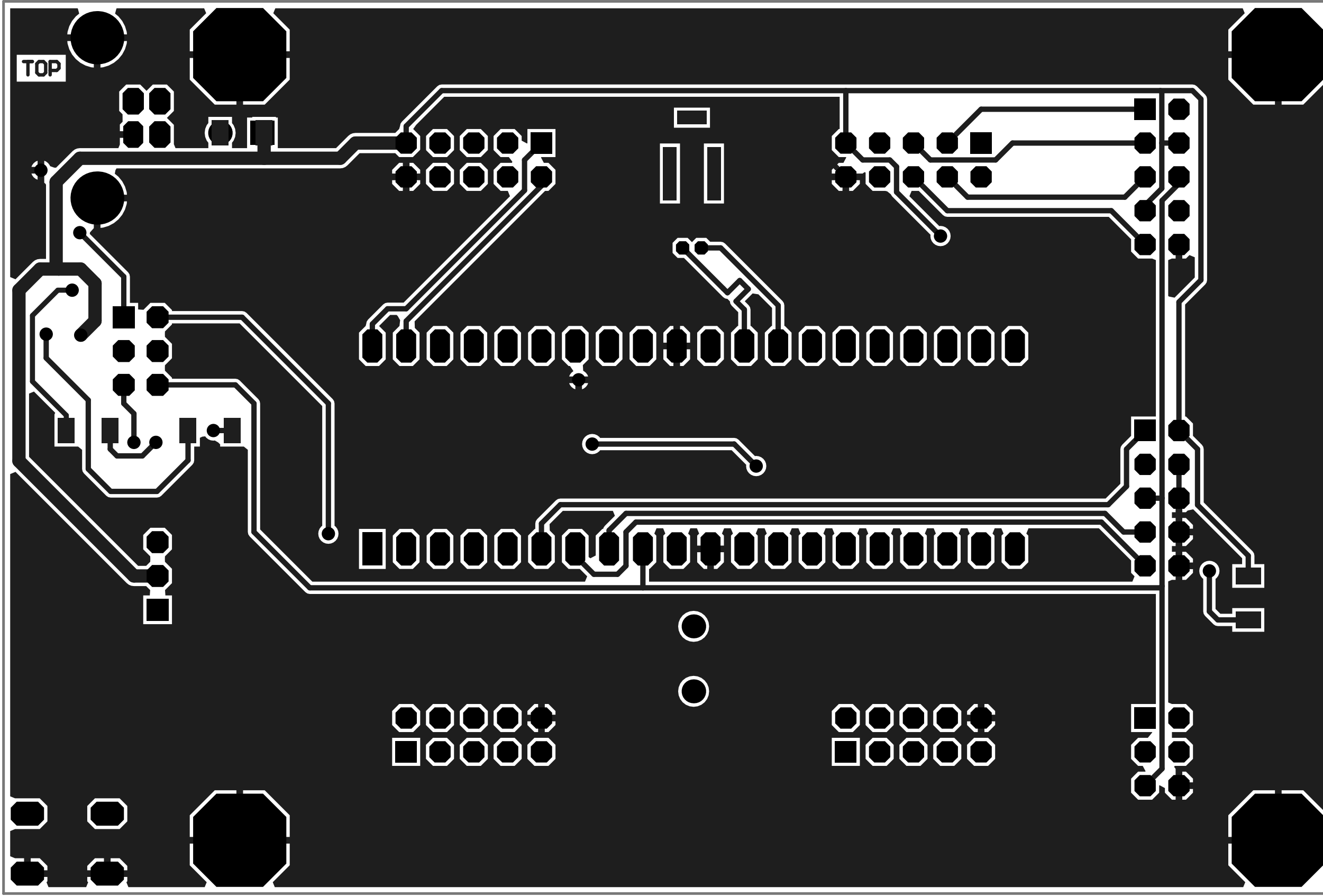
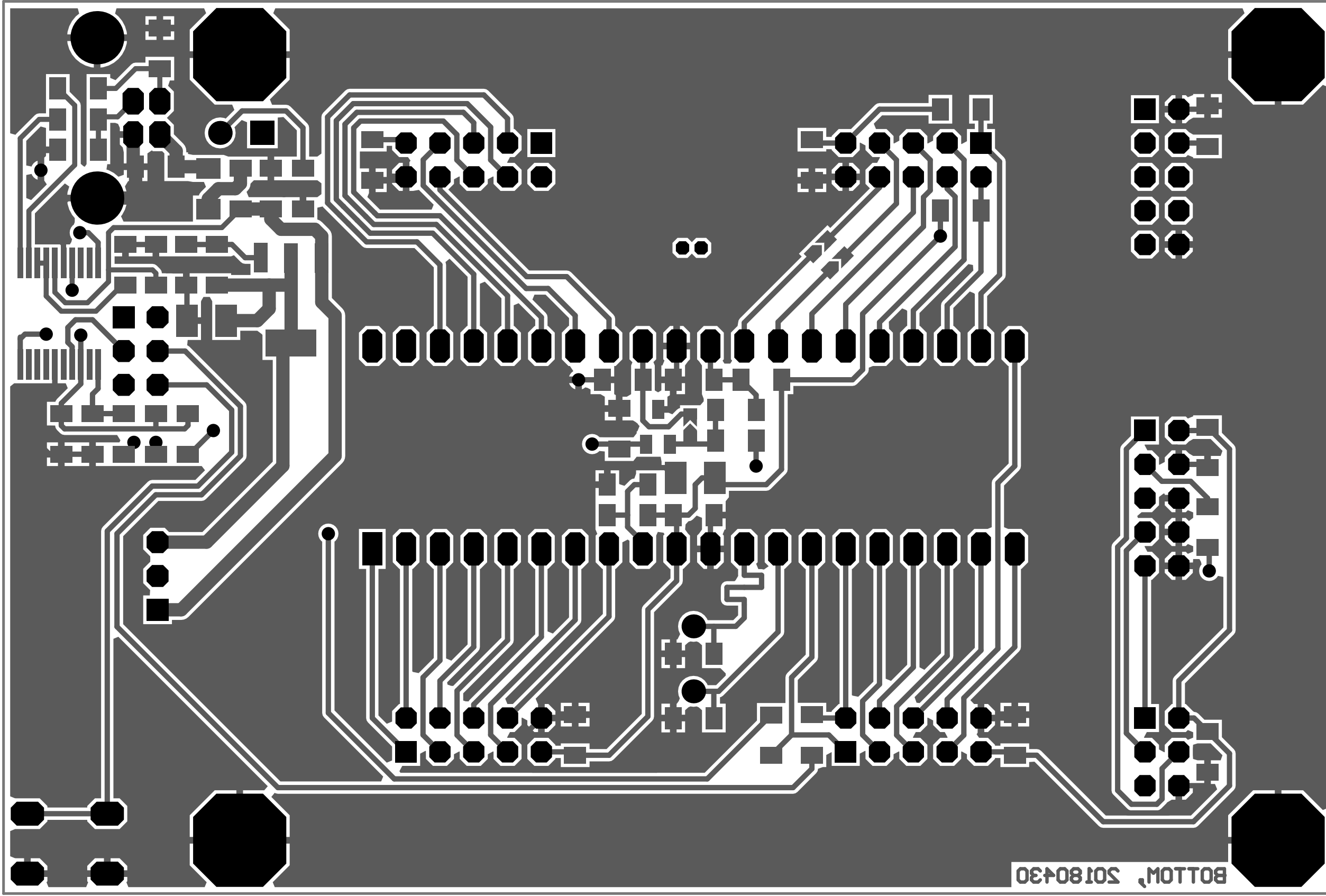




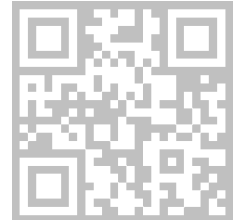
TOP







RESET


open source
hardware




TARGET
VOLTAGE


3V3
5V0


RxTx

TxD
RxD
nDTR




RxD0
TxD0
nRST

USB-UART
CONVERTER


POWER

<http://bit.ly/2wq4n0I>

PORT B




0: T0
2: AIN0
4: nSS
6: MISO
VCC

1: CLK0
3: AIN1
5: MOSI
7: SCK
GND

PORT B

PORT A



GND
7: ADC7
5: ADC5
3: ADC3
1: ADC1


VCC
6: ADC6
4: ADC4
2: ADC2
0: ADC0

PORT A

ATmega164PA / 324PA / 644PA



PORT D




0: RXD0
2: RXD1
4: OC1B
6: OC2B
VCC

1: TXD0
3: TXD1
5: OC1A
7: OC2A
GND

PORT D


PORT C





GND
7: TOS2
5: TDI
3: TMS
1: SDA

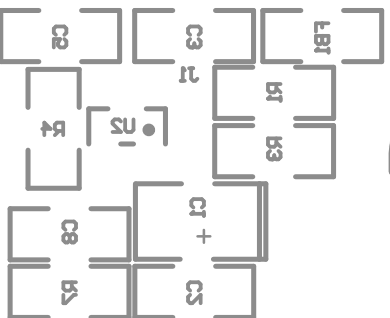
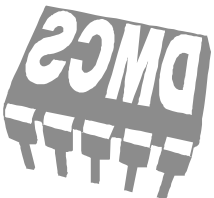
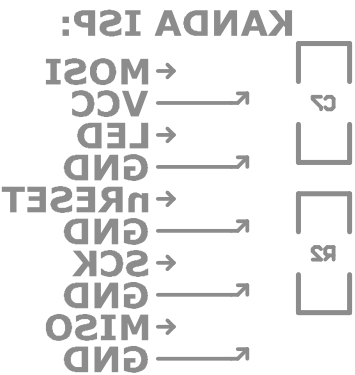
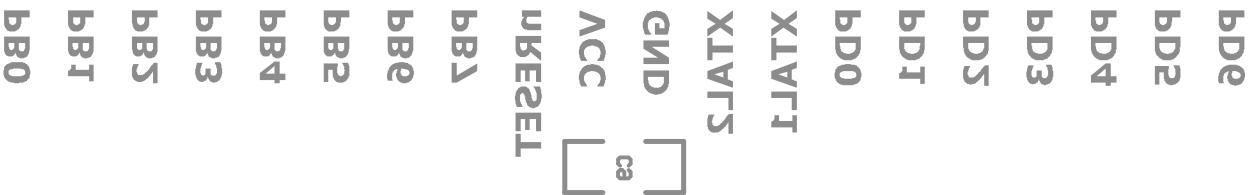
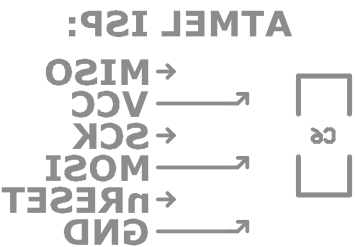
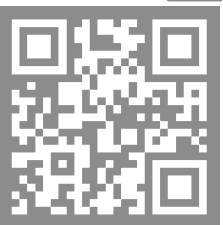
VCC
6: TOS1
4: TDO
2: TCK
0: SCL

PORT C

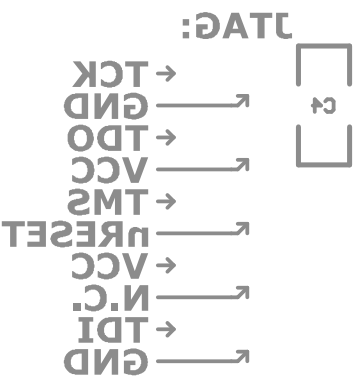

ATMEL


KANDA


JTAG



POLITECHNIKA ŁÓDZKA



13 15

